

S/N 09/852,839

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: KONDO ET AL. ✓ Examiner: V. Nguyen ✓
Serial No.: 09/852,839 ✓ Group Art Unit: 2858 ✓
Filed: May 10, 2001 ✓ Docket No.: 10873.713US01
Title: SEMICONDUCTOR INTEGRATED CIRCUIT WITH NEGATIVE
VOLTAGE GENERATION CIRCUIT, TEST METHOD FOR THE SAME,
AND RECORDING DEVICE AND COMMUNICATION EQUIPMENT
HAVING THE SAME
Customer No. 23552

CERTIFICATE UNDER 37 CFR 1.6(d):

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on August 25, 2003.

By: 

Name: Linda Engel

SUPPLEMENTAL AMENDMENT

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action of April 29, 2003, and in supplement to the Amendment and Response filed on July 29, 2003, please enter the following amendments and remarks:

Amendments to the Claims are reflected in the listing of the claims which begins on page 2 of this paper.

Remarks begin on page 10 of this paper.

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